# The RAM Model and Runtime Analysis COMS10017 - Algorithms 1

Dr Christian Konrad



## Algorithms

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- Mathematical abstraction of a computer programme



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Memory hierachy

Memory hierachy, floating point operations

Memory hierachy, floating point operations, garbage collector

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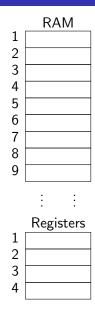
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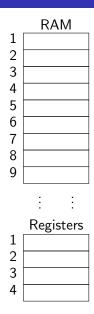
See also:

**COMS20007:** Programming Languages and Computation

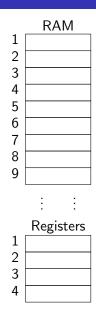


### RAM Model: Random Access Machine Model

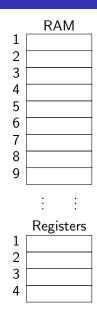
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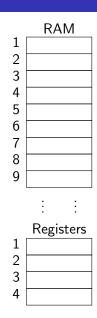
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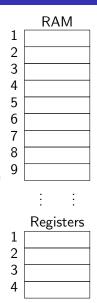
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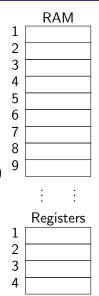
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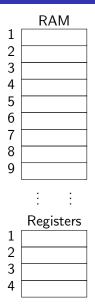


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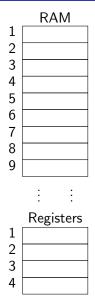


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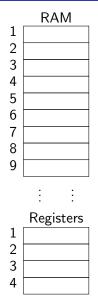


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- Compute (+, -, \*, /), bit operations, comparisons, etc. on registers
- Move a word from register to memory



# RAM Model (2)

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Exercise: How to implement in RAM model?

```
Require: Array of n integers A

S \leftarrow 0

for i = 0, ..., n - 1 do

S \leftarrow S + A[i]

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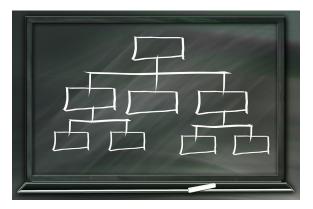
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#### Average-case

Consider a set of inputs (e.g. the set of all inputs of length n). What is the average number of elementary operations executed by the algorithm when run on every input of this set?

# Hierachy

#### **Runtime Hierachy:**



Best-case = O(Average-case) = O(Worst-case)

# Runtime/Space Analysis of Algorithms

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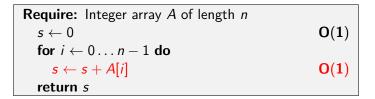
- Analyze algorithm as specified in pseudo code directly
- Make sure that every instruction can be implemented in the RAM model using O(1) elementary operations

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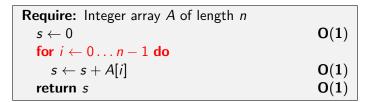
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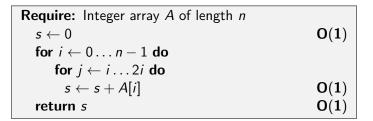
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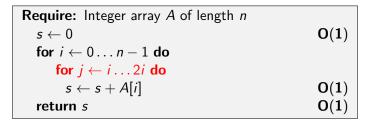
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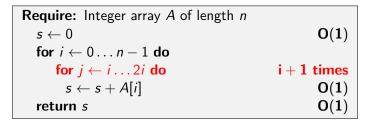
for j \leftarrow i \dots 2i do

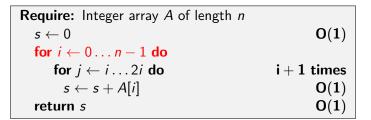
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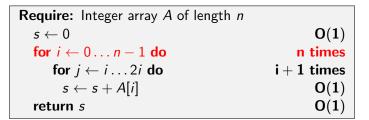
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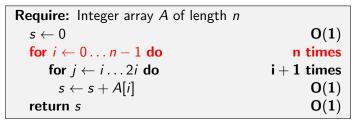












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